

Republic of Iraq

بسم الله الرحمن الرحيم

University: Diyala

College: Science

Department: Computer Sc.

Stage: First

Lecturer name: Yahya Mohammad

Qualification: Ass. Lecturer

Place of Work: Diyala

university College: Science college

The Ministry of Higher Education &
Scientific Research



Flow up of implementation cell pass play

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|--------------------|---|------------|---------|---------|------------|
| Course Instructor | Ass. Lecturer yahya Moammad | | | | |
| E-mail | Yahya_1971@yahoo.com | | | | |
| Title | Logic Design | | | | |
| Course Coordinator | | | | | |
| Course Objective | Course aims to teach students basic techniques of digital electronics | | | | |
| Course Description | | | | | |
| Textbook | Logic design | | | | |
| Course Assessments | Term Tests | Laboratory | Quizzes | Project | Final Exam |
| | As(30%) | As(20%) | | - | As (50%) |
| General Notes | | | | | |

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Course Weekly Outline

| Week | Date | Topics Covered | Lab. Experiment Assignments | Notes |
|------|-----------|------------------------|-----------------------------|-------|
| 1 | 4/2/2015 | Number Systems | | |
| 2 | 11/2/2015 | Logic Gates | | |
| 3 | 18/2/2015 | Combinational Circuits | | |
| 4 | 25/2/2015 | Combinational Circuits | | |
| 5 | 3/3/2015 | Flip Flops | | |
| 6 | 10/3/2015 | Flip Flops | | |
| 7 | 17/3/2015 | Flip Flops | | |
| 8 | 24/3/2015 | Registers | | |
| 9 | 31/3/2015 | Registers | | |
| 10 | 7/4/2015 | Registers | | |
| 11 | 14/4/2015 | Counters | | |
| 12 | 28/4/2015 | Counters | | |
| 13 | 5/5/2015 | Timing Circuits | | |
| 14 | 12/5/2015 | Timing Circuits | | |
| 15 | 19/5/2015 | Memory Systems | | |
| 16 | 26/5/2015 | Memory Systems | | |

Instructor Signature:

Dean Signature: